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Attorney Docket No. QKL004 US

Application Number 10/738,382

Filing Date December 16, 2003

First Inventor Soon-Gil Jung

Confirmation No. 7260

Group Art Unit 2838

Examiner Name Unknown

## ENCLOSURES (check all that apply)

<input type="checkbox"/> Fee Transmittal Form (1 page in duplicate)	<input type="checkbox"/> Assignment Papers (for an Application)	<input type="checkbox"/> After Allowance Communication to Group
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## SIGNATURE OF APPLICANT, ATTORNEY OR AGENT

Firm or Individual Name	Michael J. Halbert (Reg. No. 40,633) Silicon Valley Patent Group LLP 2350 Mission College Boulevard, Suite 360 Santa Clara, California 95054
Signature	
Date	12-17-04

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DEC 23 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Soon-Gil Jung  
Assignee: QuickLogic Corporation  
Title: Regulator with Variable Capacitor for Stability Compensation  
Serial No.: 10/738,382 Filing Date: December 16, 2003  
Examiner: Unknown Group Art Unit: 2838  
Docket No.: QKL004 US Confirmation No: 7260

Santa Clara, California  
December 17, 2004

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**INFORMATION DISCLOSURE STATEMENT  
UNDER 37 CFR §1.97(b)**

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, §1.97 and §1.98, the Applicants submit for consideration in the above-identified patent application the document listed on the accompanying Form PTO-1449. Copies of these documents are also submitted herewith. The Examiner is requested to make these documents of record.

This Information Disclosure Statement is submitted pursuant to 37 CFR §1.97(b) as it within three months of the filing date of a national application other than a continued prosecution application and/or before the mailing of a first Office Action on the merits. Accordingly, no fee is required.

Applicants would appreciate the Examiner initialing and returning the Form PTO-1449, indicating that the information has been considered and made of record herein.

The information contained in this Information Disclosure Statement is to the best of my knowledge and is not to be construed as a representation that: (i) a complete search has been made; (ii) additional information material to the examination of this application does not

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exist; (iii) the information, protocols, results and the like reported by third parties are accurate or enabling; or (iv) the above information constitutes prior art to the subject invention.

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Michael J. Halbert

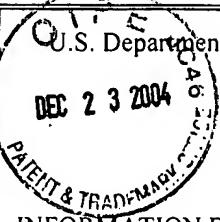
Respectfully submitted,



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 <p>U.S. Department of Commerce, Patent and Trademark Office</p> <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)</p>		Application No.:	10/738,382
		Filing Date:	December 16, 2003
		First Named Inventor:	Soon-Gil Jung
		Group Art Unit:	2838
		Examiner Name:	Unknown
		Confirmation No.:	7260
		Attorney Docket No.:	QKL004 US

U.S. Patent Documents							
*Examiner Initials	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate	
Foreign Patent Documents							
							Translation
	Document	Date	Country	Class	Subclass	Yes	No
Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)							
	1.	Gabriel A. Rincon-Mora et al., "A Low Voltage, Low Quiescent Current, Low Drop-Out Regulator", IEEE Journal of Solid State Circuits, Vol. 33, No. 1, January 1998, pp 36-44					
	2.	Ka Nang Leung et al., "A Capacitor-Free CMOS Low-Dropout Regulator With Damping-Factor-Control Frequency Compensation", Journal of Solid State Circuits, Vol. 38, No. 10, October 2003, pp 1691-1702					

Examiner:	Date Considered:
<p>* Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication with applicant.</p>	